

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	reconf\$6 with test with system same transceiver and "front end"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:53
L2	0	(reconf\$6 with test with system) same transceiver and "front end"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:53
L3	9	(reconf\$6 with system) same transceiver and "front end"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:54
L4	474	(program\$5 with block with diagram) same FPGA	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:54
L5	773	reconfig\$ with test same system	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:54
L6	17	I4 and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 09:55



reconfigure test system transceiver front end

Search

[Advanced Scholar Search](#)[Scholar Preferences](#)[Scholar Help](#)**Scholar**Results 1 - 10 of about 215 for reconfigure test system transceiver front end. (0.08 seconds)The APS Control **System** Network - group of 4 »KV Sidorowicz, WP McDowell - [adwww.fnal.gov](http://adwww.fnal.gov)... This allows us to **reconfigure** the network if future technology, such as ATM ... the insertion devices and to run the global orbit correction **system**. ... NETWORK TESTING ...Cited by 2 - [View as HTML](#) - [Web Search](#)AN UPDATE ON AN INTEGRATED CNI **SYSTEM**-TIESLESGB Heal - [pdf.aiaa.org](http://pdf.aiaa.org)... first what makes up a modern **transceiver**, on the ... it would be a simple task to **reconfigure** the **system** ... The **test system** controller is capable of taking control of ...[Web Search](#)Re-configurable Multi-mode Radio Architectures for enhanced 3G Terminals - group of 2 »K Strohmenger, R Kakerow, F Henkel, D Köther - [nokia.com](http://nokia.com)... to achieve similar performance for the **test** cases defined ... has been shown and techniques to **reconfigure** circuits and ... A new approach for accurate **System** Level RF ...[View as HTML](#) - [Web Search](#)CMOS Analog Design for Wireless Communication - group of 3 »V Liberali, G Trucco - Microelectronics, 2004. 24th International Conference on, 2004 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)... way it is possible to **reconfigure** the **system** to ... ARCHITECTURES AND CIRCUIT SOLUTIONS In wireless **systems**. ... choice of the optimum **transceiver** architecture depends ...[Web Search](#)Re-configurableMulti-modeRadio Architecturesforenhanced3G Terminals œ ABSTRACTœAG Terminals - [mumor.org](http://mumor.org)... to achieve similar performance for the **test** cases defined ... has been shown andtechniques to **reconfigure** circuits andsub ... A new approach for accurate **System** Level RF ...[View as HTML](#) - [Web Search](#)Electronically Tunable Antenna Pair and Novel RF **Front-End** Architecture for Software-Defined Radios - group of 4 »SH OH, JT ABERLE, S ANANTHARAMAN, K ARAI, HANL ... - EURASIP Journal on Applied Signal Processing, 2005 - [hindawi.net](http://hindawi.net)... required switch control signals to **reconfigure** the antenna ... Figure 9: **Test** setup for demonstrating the ... of the an- tenna, we performed a **system** simulation using ...[View as HTML](#) - [Web Search](#)Design and Implementation of Re-configurable **transceiver** for cdma2000M Sheen, S Lee, J Kim - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)... In the client DM, we can **reconfigure** the SDR ... For the **test** of reconfiguration, we implemented the ... **transceiver** based on commercial cdma2000 **system** was developed ...[Web Search](#)IMPLEMENTING PRODUCTION **TEST** SYSTEMS WITH THE PXI ARCHITECTURE - group of 2 »M Dewey, I GenRad - Etronix Conference, 2001 - [teradyne.com](http://teradyne.com)... this **system** allows the **test** engineer the option to adjust and **reconfigure** the **system** ... Figure 5 6U PXI **Test System** ... Multiplexer Tx /Rx OC-192 Optical **Transceiver** ...Cited by 1 - [View as HTML](#) - [Web Search](#)

Smart Dust Mote Core Architecture - group of 7 »

B Warneke, S Bhavé - CS252, Spring2000: Project Report. Berkeley Sensor and ... - [www-bsac.eecs.berkeley.edu](http://www-bsac.eecs.berkeley.edu)

... In typical low power mixed-signal **systems**, most designers ... an immediate action and those that **reconfigure** the mote ... of some of the blocks to **test** the feasibility ...

Cited by 1 - View as HTML - Web Search

A 2.4-GHz Dual-Mode 0.18-

TB Cho, D Kang, CH Heng, BS Song - IEEE JOURNAL OF SOLID-STATE CIRCUITS, 2004 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... and therefore it is easier to **reconfigure** the **system** ... when the **transceiver** is integrated with another **system** running at a ... 19(b). To **test** the image rejection in ...

Web Search - BL Direct

Google

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

reconfigure test system transceive

[Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2006 Google